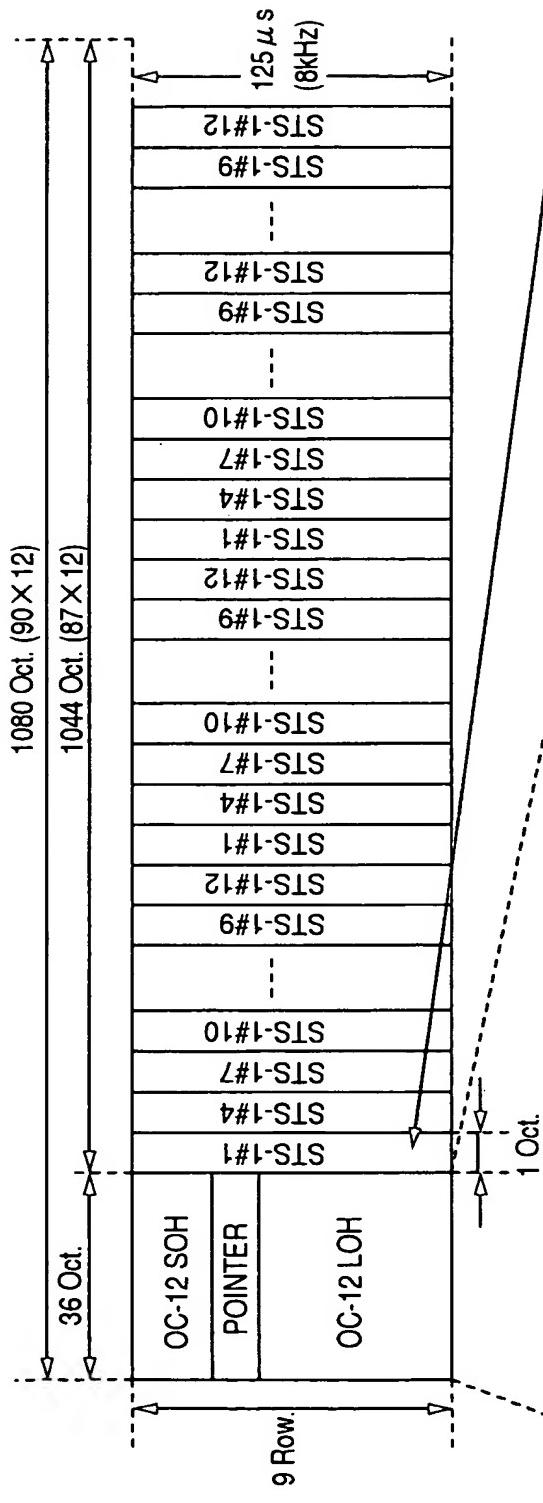


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POH	1	J1	2	B3	3	C2	4	G1	5	F2	6	H4	7	Z3	8	Z4	9	Z5	10	3 Oct.	3 Oct.
1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	A1	11 Oct.	11 Oct.
2	B1	X	X	X	X	X	X	E1	X	X	X	X	X	F1	X	X	X	X	X	3 Oct.	3 Oct.
3	D1	X	X	X	X	X	X	X	D2	X	X	X	X	D3	X	X	X	X	X	3 Oct.	3 Oct.
4	H1	H1	H1	H1	H1	H1	H1	H1	H1	H2	H2	H2	H2	H2	H3	H3	H3	H3	H3	3 Oct.	3 Oct.
5	B2	B2	B2	B2	B2	B2	B2	B2	K1	X	X	X	X	K2	X	X	X	X	X	3 Oct.	3 Oct.
6	D4	X	X	X	X	X	X	X	D5	X	X	X	X	X	X	X	X	X	X	3 Oct.	3 Oct.
7	D7	X	X	X	X	X	X	X	D8	X	X	X	X	X	X	X	X	X	X	3 Oct.	3 Oct.
8	D10	X	X	X	X	X	X	X	D11	X	X	X	X	X	X	X	X	X	X	3 Oct.	3 Oct.
9	S1	Z1	Z2	Z2	Z2	Z2	M1	Z2	Z2	Z2	Z2	E2	X	X							

FIG. 2

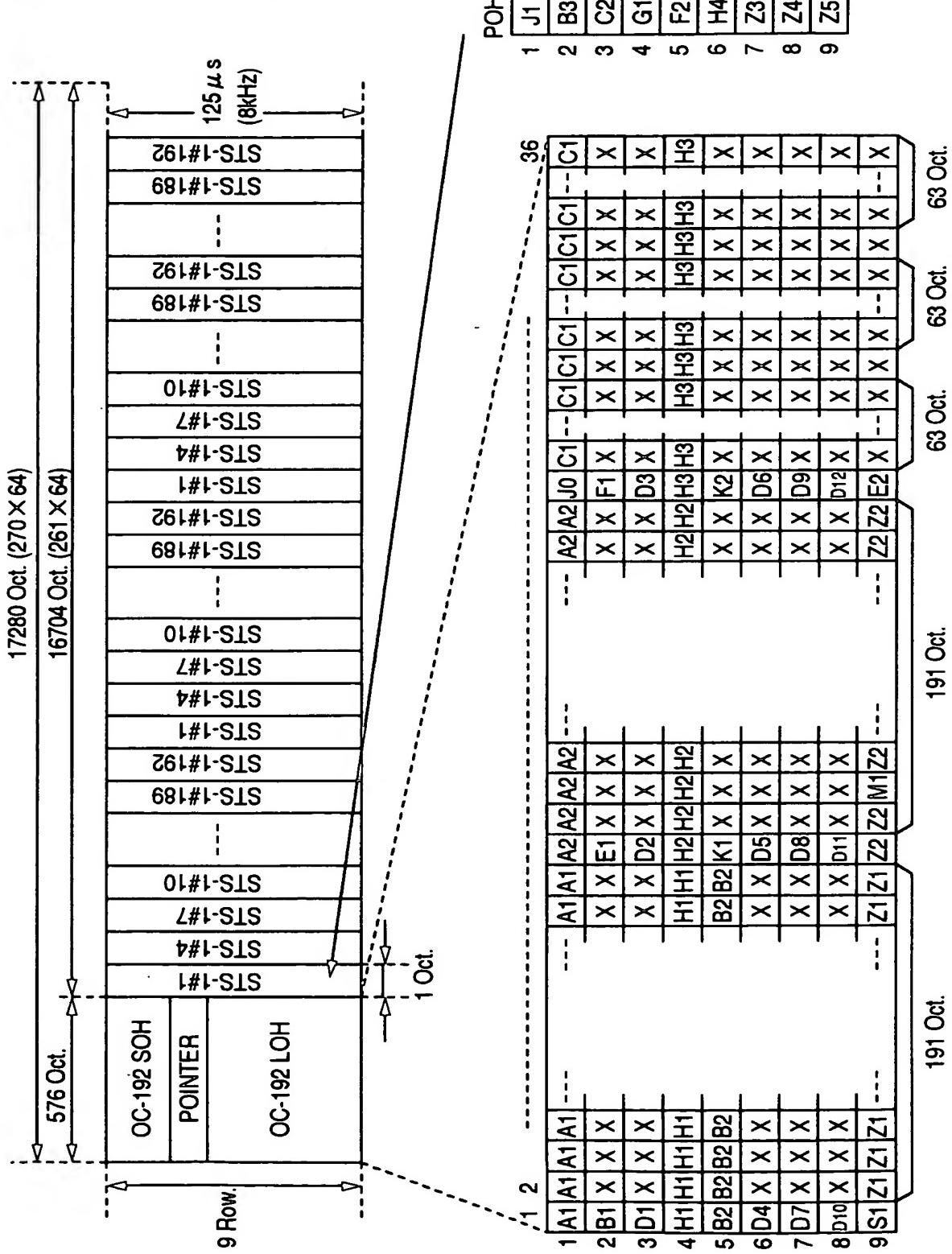
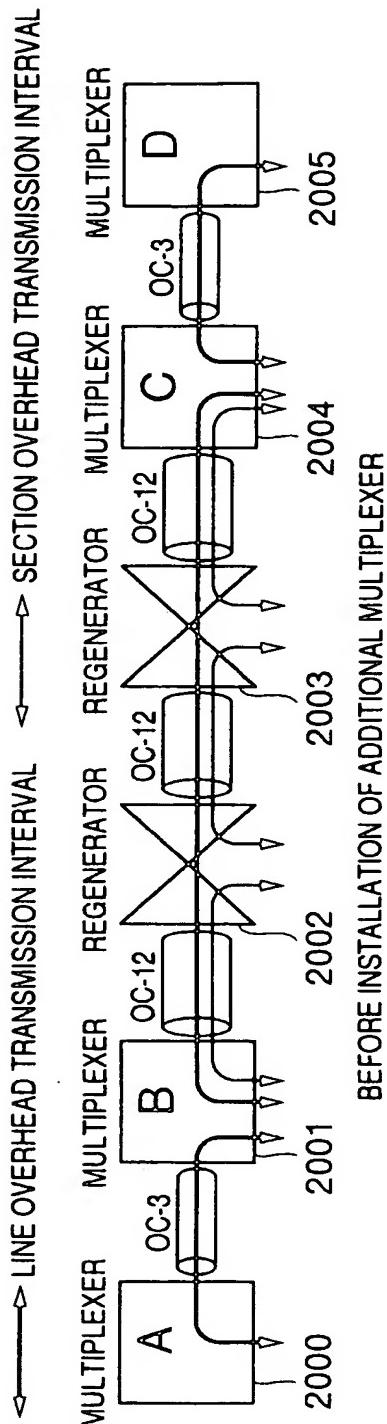
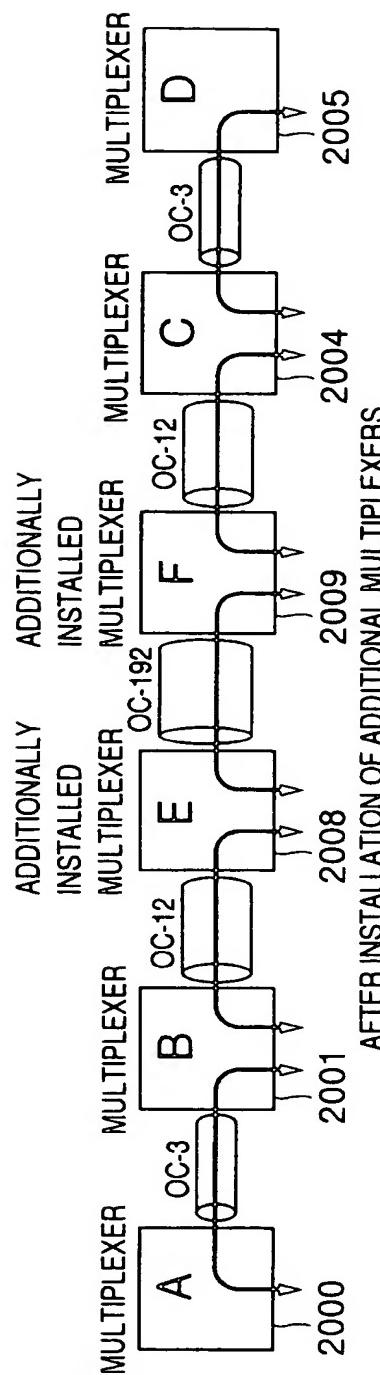


FIG.3

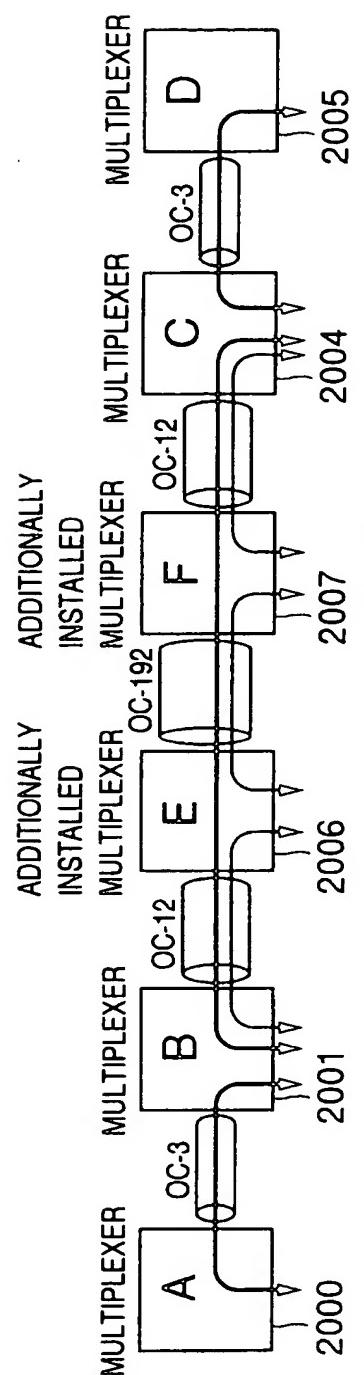
OVERHEAD		FUNCTION
SECTION OVERHEAD	A1,2A	FRAME SYNCHRONIZATION
	B1	ADMINISTRATION OF ERRORS IN SECTION INTERVAL
	D1~D3	USED FOR MAINTENANCE OPERATION
	E1	SPEECH COMMUNICATION FOR CRAFT MAN
	J0 (C1)	DESIGNATION OF OC-1 MULTIPLEX NUMBER OF OC-M SIGNAL
	F1	USED FOR CONVENIENCE OF CRAFT MAN
LINE OVERHEAD	H1,H2	INDICATION OF HEAD PHASE OF PATH
	H3	USED FOR FREQUENCY SYNCHRONIZATION
	B2,M1	ADMINISTRATION OF ERRORS IN LINE INTERVAL
	K1,K2	SWITCHING CONTROL FOR LINE INTERVAL,TRANSFER OF ALARM
	D4~D12	USED FOR MAINTENANCE OPERATION
	E2	SPEECH COMMUNICATION FOR CRAFT MAN
	S1	USED FOR OPERATION OF NETWORK SYNCHRONIZATION
	Z1,Z2	INTERNATIONALLY RESERVED AS SPARE



BEFORE INSTALLATION OF ADDITIONAL MULTIPLEXER

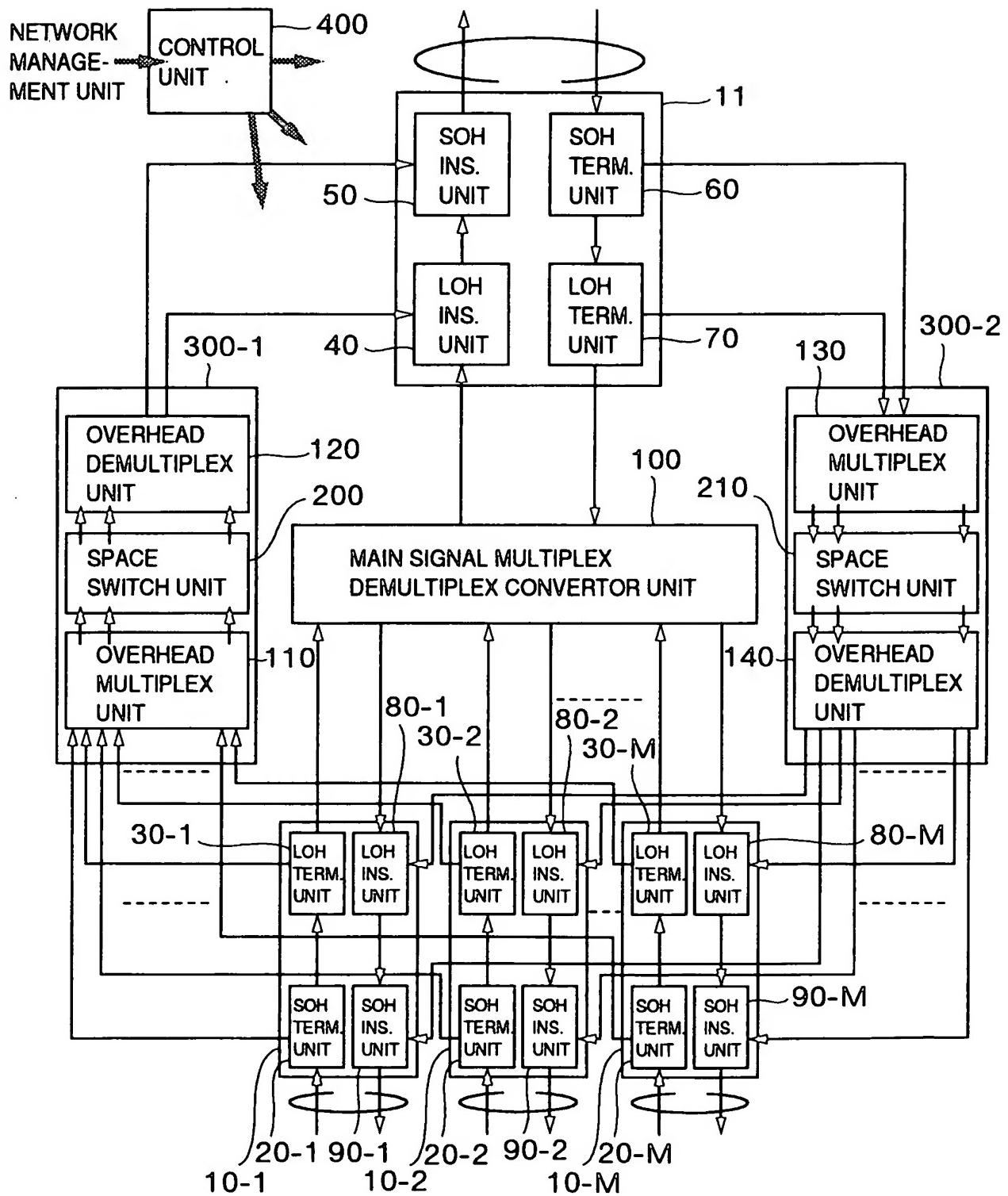


AFTER INSTALLATION OF ADDITIONAL MULTIPLEXERS



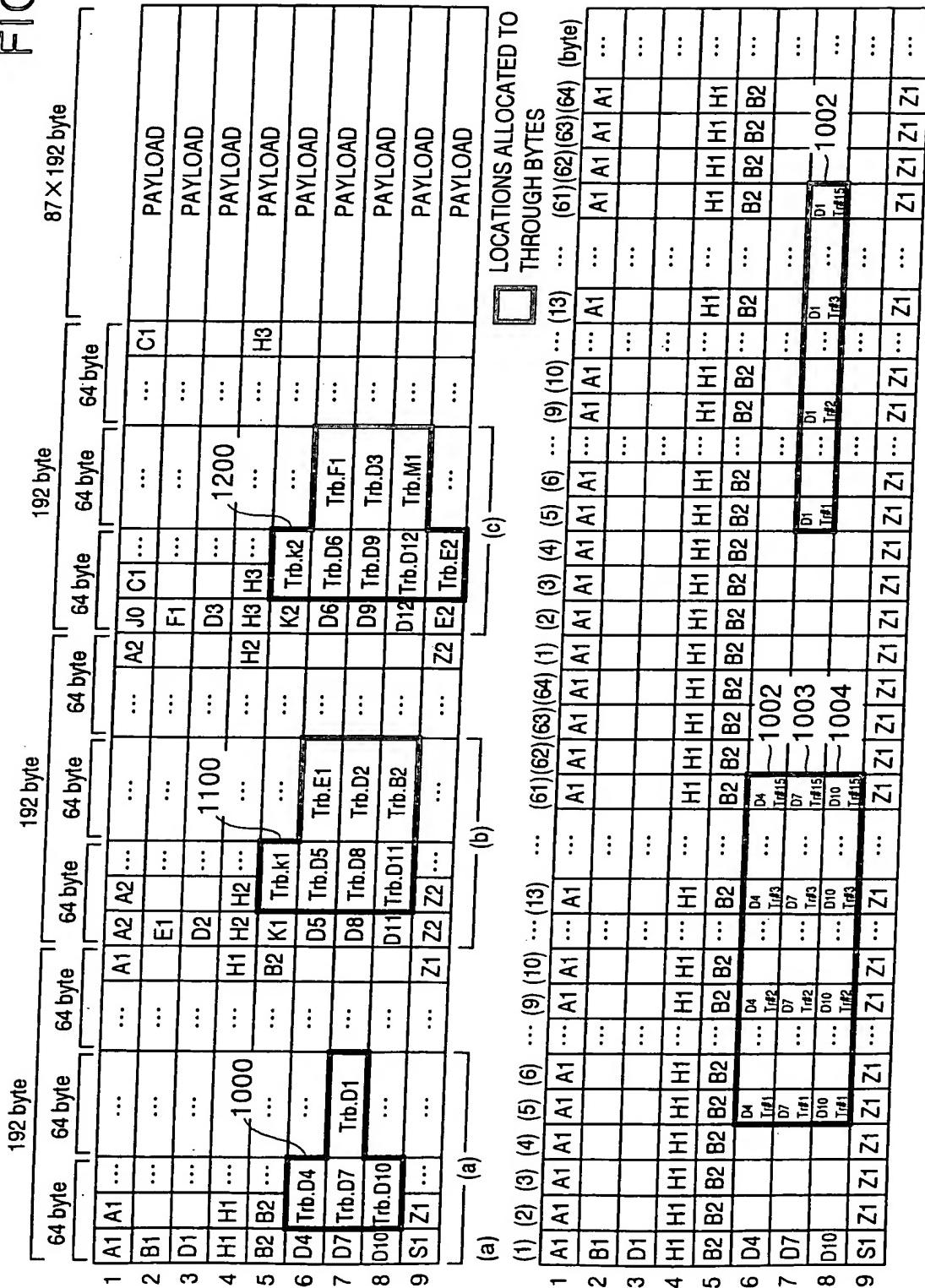
AFTER INSTALLATION OF ADDITIONAL MULTIPLEXERS
ACCORDING TO PRESENT INVENTION
ACCORDING TO PRESENT INVENTION

FIG.5



10G FRAME

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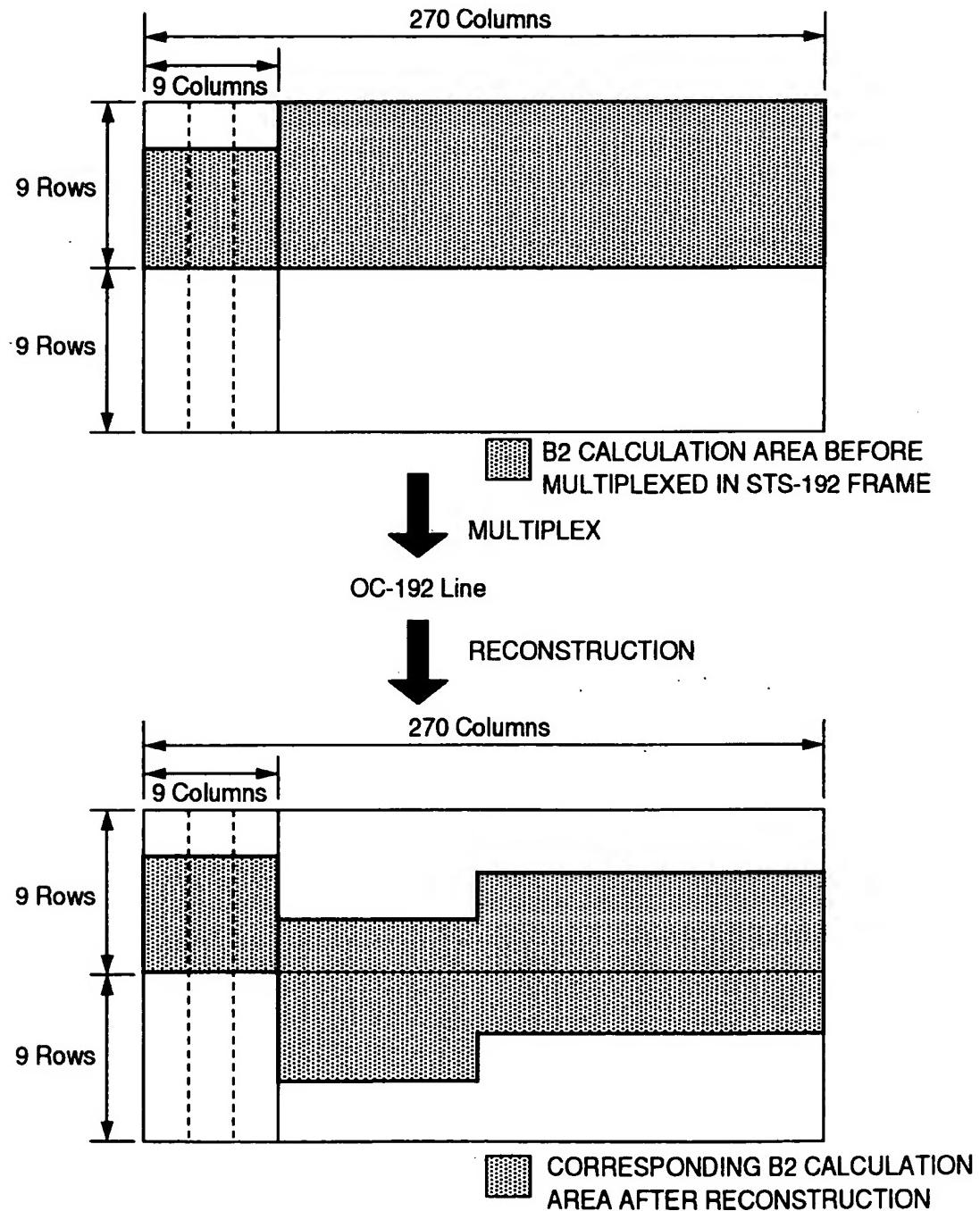
↓ (CONTINUED ON NEXT PAGE)

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↑ (CONTINUED FROM PREVIOUS PAGE) ↑

(b)		(1)	(2)	(3)	(4)	(5)	(6)	(9)	(10)	(13)	...	(61)	(62)	(63)	(64)	(1)	(2)	(3)	(4)	(5)	(6)	...	(9)	(10)	(13)	...	(61)	(62)	(63)	(64)	(byte)
1	A2	A2	A2	A2	A2	A2	...	A2	A2	A2	...	A2	A2	A2	A2	A2	A2	A2	A2	A2	...	A2	A2	A2	A2	...	A2	A2	A2	A2	...
2	E1					
3	D2					
4	H2	H2	H2	H2	H2	H2	...	H2	H2	H2	...	H2	H2	H2	H2	H2	H2	H2	H2	H2	...	H2	H2	H2	H2	...	H2	H2	H2	H2	...
5	K1						KT				H2										
6	D5						T#1				K1										
6	D5						D5				T#2										
7	D8						D5				T#1										
7	D8						D5				T#2										
8	D11						D8				T#1										
8	D11						D8				T#2										
9	Z2	Z2	M1	Z2	Z2	Z2	...	Z2	Z2	Z2	...	Z2	Z2	Z2	Z2	Z2	Z2	Z2	Z2	Z2	...	Z2	Z2	Z2	Z2	...	Z2	Z2	Z2	Z2	...

FIG.8



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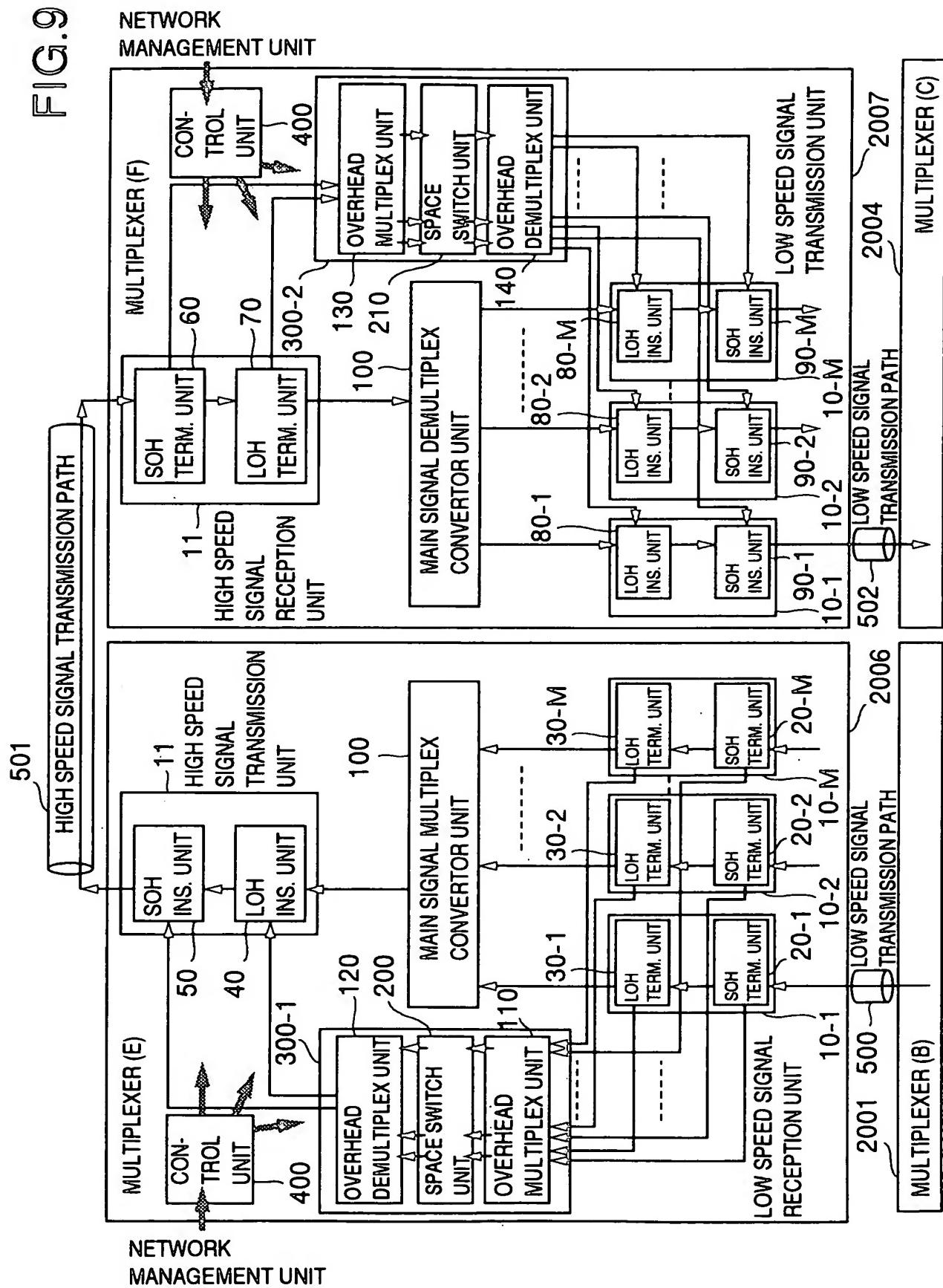
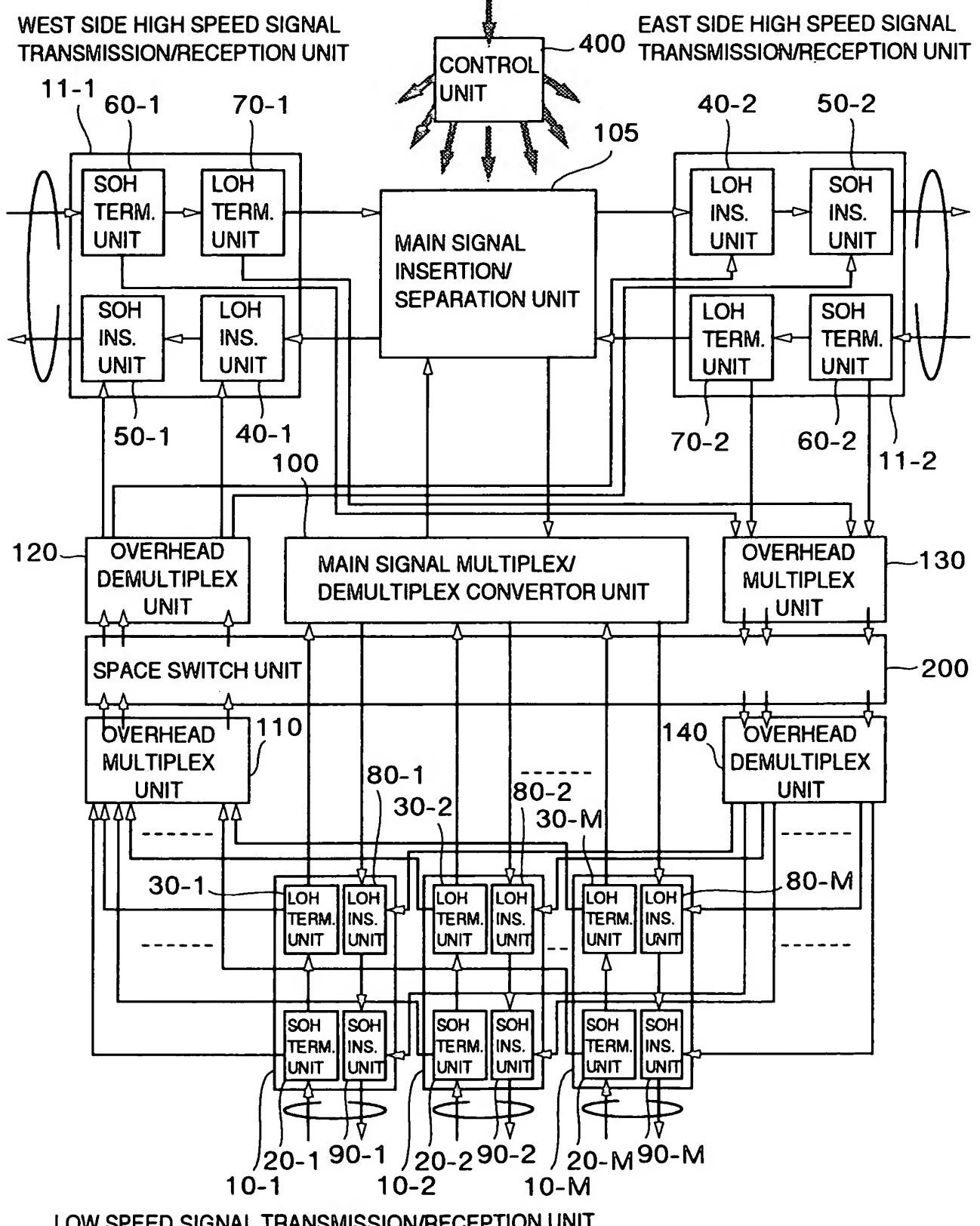


FIG.10

NETWORK MANAGEMENT UNIT



LOW SPEED SIGNAL TRANSMISSION/RECEPTION UNIT

FIG.11A

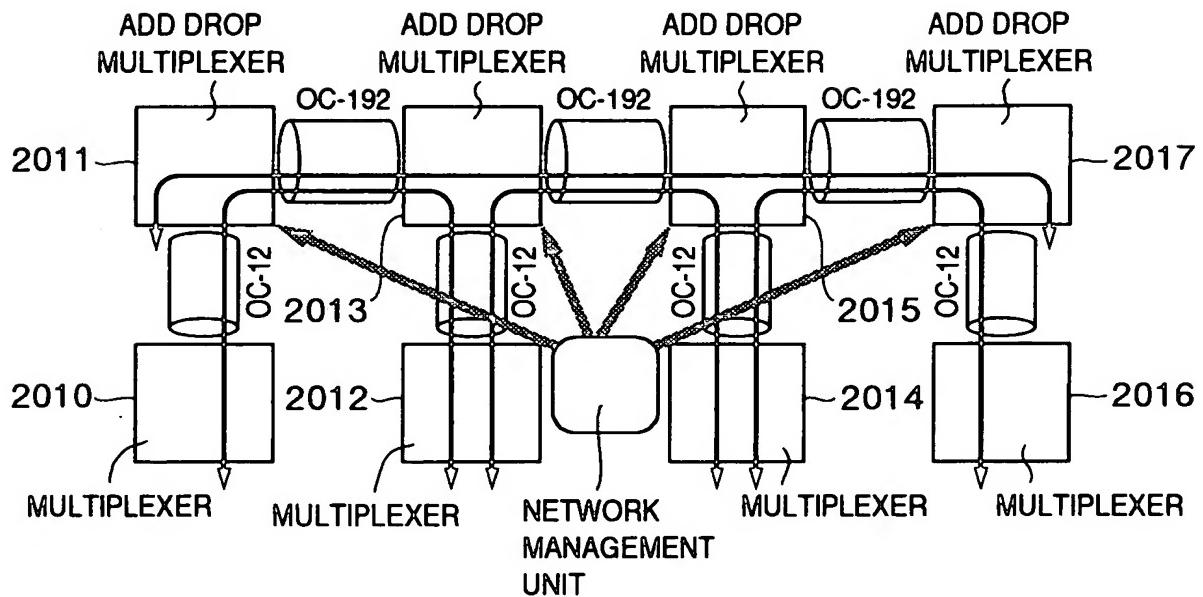


FIG.11B

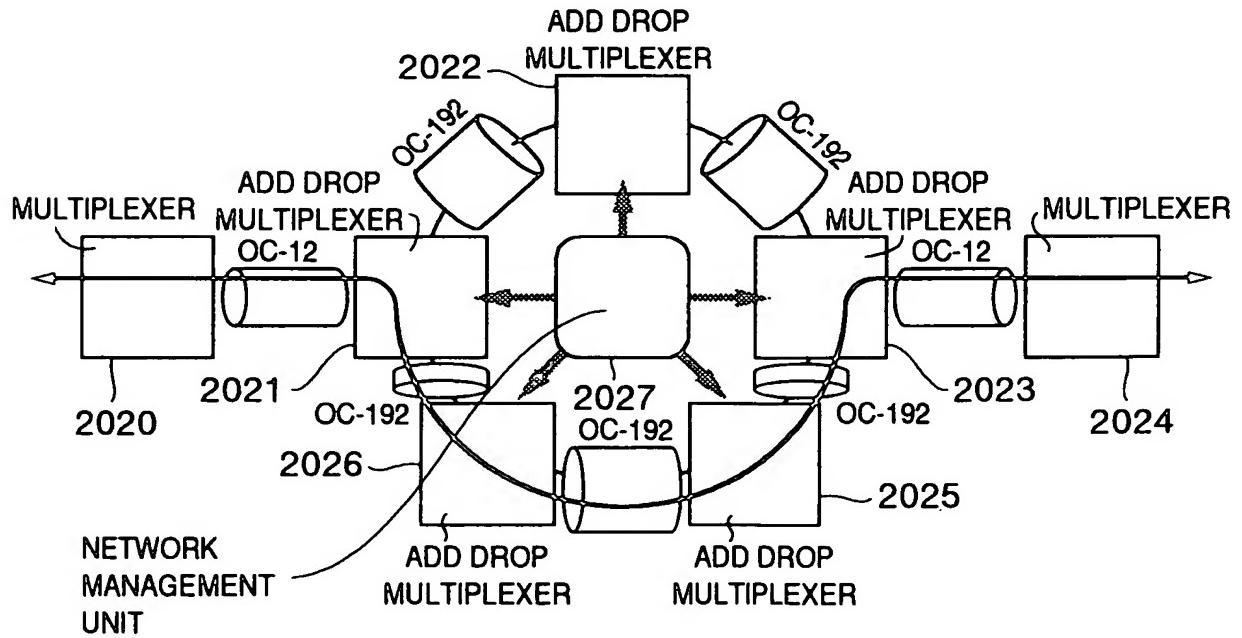


FIG. 12A

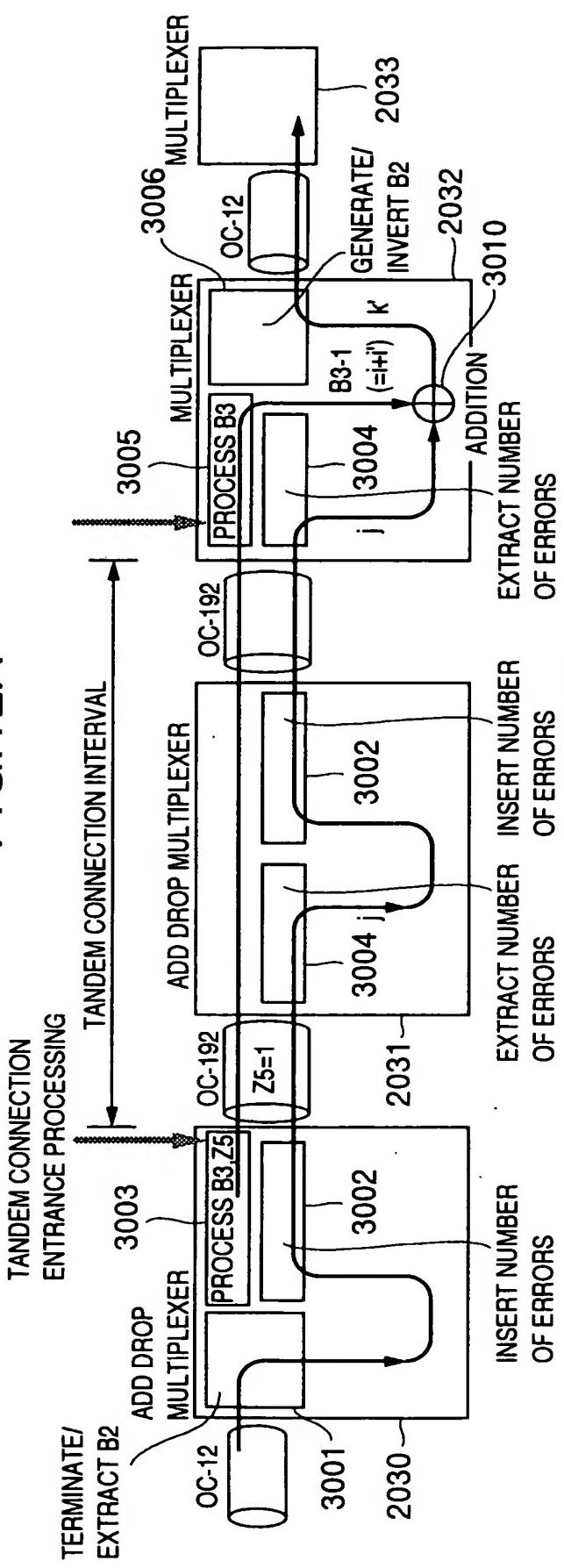


FIG. 12B

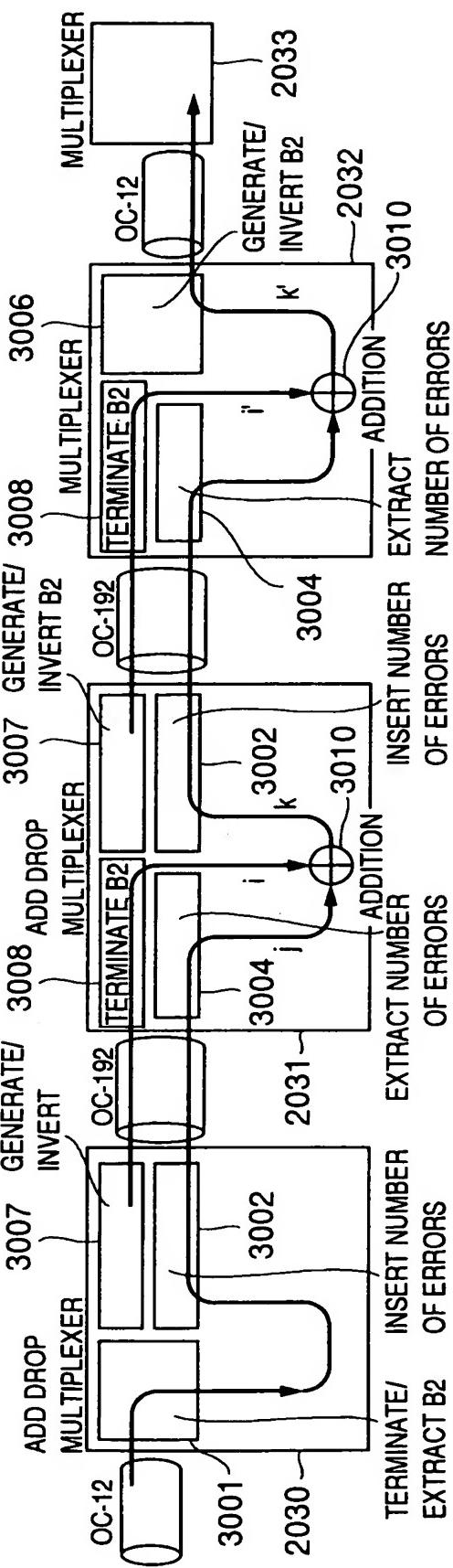


FIG. 13

